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| APPLICATION NO.                               | FILING DA  | TE        | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|---|------------|-----------|----------------------|---------------------|------------------|
| 10/698,169                                    | 10/31/2003 |           | Sheldon Aronowitz    | 02-6037/LSI1P218    | 9839             |
| 75  | 90 11      | 1/19/2004 |                      | EXAMINER            |                  |
| LSI Logic Corporation                         |            |           |                      | MAI, ANH D          |                  |
| 1551 McCarthy Boulevard<br>Milpitas, CA 95035 |            |           |                      | ART UNIT            | PAPER NUMBER     |
|   |            |           |                      | 2814                |                  |

DATE MAILED: 11/19/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

|  | Application No.   | Applicant(s)   |                  |  |
|--|---|--|------------------|--|
|  | 10/698,169  | ARONOWITZ ET A   | ARONOWITZ ET AL. |  |
| Office Action Summary  | Examiner  | Art Unit   |                  |  |
|  | Anh D. Mai  | 2814   | مهم              |  |
| The MAILING DATE of this communication ap<br>Period for Reply  | pears on the cover sheet wi   | th the correspondence add  | dress            |  |
| A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a report of the period for reply is specified above, the maximum statutory period for reply within the set or extended period for reply will, by statuted the period for reply will be set or extended period for rep | 136(a). In no event, however, may a re<br>oly within the statutory minimum of thirt<br>I will apply and will expire SIX (6) MON<br>le, cause the application to become AB | eply be timely filed y (30) days will be considered timely THS from the mailing date of this co ANDONED (35 U.S.C. § 133). |                  |  |
| Status   |   |  |                  |  |
| 1)⊠ Responsive to communication(s) filed on 31 (   | October 2003.   |  |                  |  |
| 2a) ☐ This action is <b>FINAL</b> . 2b) ☐ Thi  | is action is non-final.   |  |                  |  |
| 3) Since this application is in condition for allows closed in accordance with the practice under  | ·   |  | merits is        |  |
| Disposition of Claims  |   |  |                  |  |
| 4) ⊠ Claim(s) 1-20 is/are pending in the application 4a) Of the above claim(s) is/are withdra 5) □ Claim(s) is/are allowed. 6) □ Claim(s) is/are rejected. 7) □ Claim(s) is/are objected to. 8) ⊠ Claim(s) 1-20 are subject to restriction and/or  | awn from consideration.   |  |                  |  |
| Application Papers   |   |  |                  |  |
| 9) The specification is objected to by the Examin 10) The drawing(s) filed on is/are: a) ac  | cepted or b) □ objected to  |  |                  |  |
| Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the E   | ction is required if the drawing  | (s) is objected to. See 37 CF  |                  |  |
| Priority under 35 U.S.C. § 119   |   |  |                  |  |
| 12) Acknowledgment is made of a claim for foreig  a) All b) Some * c) None of:  1. Certified copies of the priority documer  2. Certified copies of the priority documer  3. Copies of the certified copies of the priority documer  application from the International Burea  * See the attached detailed Office action for a list  | nts have been received.<br>nts have been received in A<br>ority documents have been<br>au (PCT Rule 17.2(a)).   | pplication No received in this National  | Stage            |  |
| Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08 Paper No(s)/Mail Date   | Paper No(s  | Summary (PTO-413)<br>s)/Mail Date<br>nformal Patent Application (PTC   | D-152)           |  |

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## **DETAILED ACTION**

## Election/Restrictions

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:

 Claims 1-11, Group I, drawn to semiconductor device, classified in class 257, subclass 411.

II. Claims 12-20, Group II, drawn to method of making, classified in class 438, subclass 287.

The inventions are distinct, each from the other because of the following reasons:

- 2. Inventions Group I and II are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case the method can be used to make different memory device including forming a floating gate over the channel region then forming an electron trapping material over the floating gate followed by forming a control gate electrode connected with the top portion of the gate stack.
- 3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.
- 4. This application contains claims directed to the following patentably distinct species of the claimed invention:

Species A, the gate dielectric layer comprises an entirety of the trapping material, as shown in Fig. 3a.

Species B, the gate dielectric layer comprises the trapping material sandwiched between two silicon oxide layers, as shown in Fig. 2.

Species C, the gate dielectric layer comprises the trapping material sandwiched between two interface layers, wherein the interface layers comprises the electron trapping material and silicon, as shown in Fig. 3b.

Species D, the gate dielectric layer comprises the trapping material sandwiched between a silicon oxide layer and the gate electrode, as shown in Fig. 4.

Applicant is required under 35 U.S.C. 121 to elect a single disclosed species for prosecution on the merits to which the claims shall be restricted if no generic claim is finally held to be allowable. Currently, claims 1 and 12 are generic.

Applicant is advised that a reply to this requirement must include an identification of the species that is elected consonant with this requirement, and a listing of all claims readable thereon, including any claims subsequently added. An argument that a claim is allowable or that all claims are generic is considered nonresponsive unless accompanied by an election.

Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which are written in dependent form or otherwise include all the limitations of an allowed generic claim as provided by 37 CFR 1.141. If claims are added after the election, applicant must indicate which are readable upon the elected species. MPEP § 809.02(a).

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Should applicant traverse on the ground that the species are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the species to be obvious variants or clearly admit on the record that this is the case. In either instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C. 103(a) of the other invention.

- 5. Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).
- 6. Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anh D. Mai whose telephone number is (571) 272-1710. The examiner can normally be reached on 9:00AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

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applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Anh D. Mai

November 15, 20